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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Chen, et al.

Serial No.: 09/748,256 Group Art Unit: 2811

Filed: December 27, 2000 Examiner: Ori Nadav

For: METHOD FOR FABRICATING COMPLEMENTARY METAL OXIDE
SEMICONDUCTOR (CMOS) DEVICES ON A MIXED BULK AND SILICON-ON-
INSULATOR (SOI) SUBSTRATE

Honorable Commissioner of Patents
Alexandria, VA 22313-1450
BOX AF

AMENDMENT UNDER 37 C.F.R. § 1.116

Sir:

In response to the Final Office Action dated November 19, 2003, please amend the
above-identified application as follows: